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Rev conrol

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Rev : 3.0-A

Changes compared to Rev 2.0-A

1. Swapped the SCK/CS lines for the dual FTDI (bug in design).
2. Exchanged NWP port with the RS232. Now NWP port is available on Quad UART Port-4.
3. Added option to route the NWP logger to Dual UART on LP boards.
4. Added option to flash the CC3100BP while mated on MCU LPs.
5. Added jumper to disconnect 5V when connected with LP
6. Disconnected the UART for RT3 by default using DNP resistors

C

C

B

B

A

A

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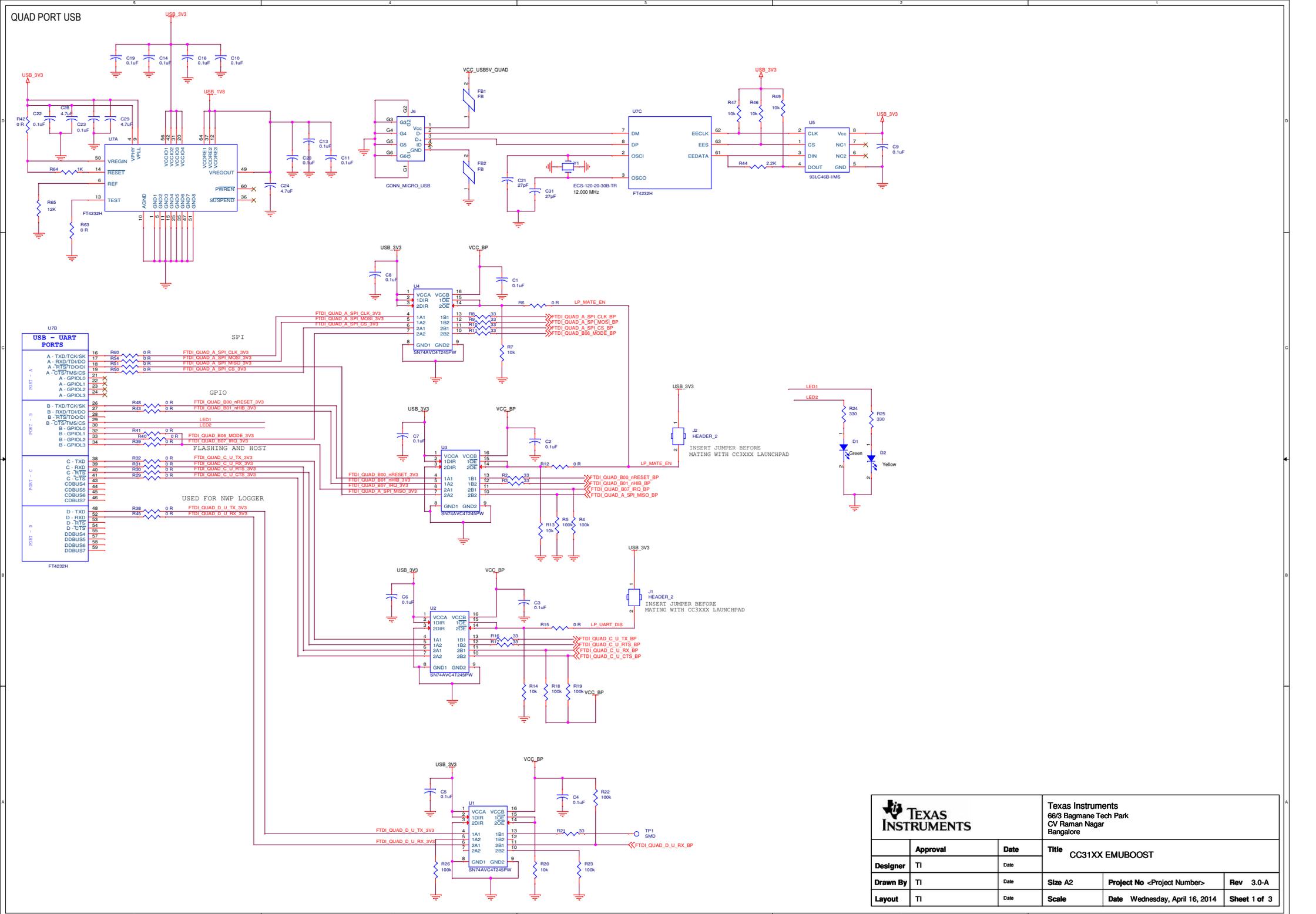
4

3

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QUAD PORT USB



U79

USB - UART PORTS

PORT - A	Pin	Resistor	Signal
A - TXD/TK/SK	16	R60	0 R
A - RXD/TD/DO	17	R54	0 R
A - RTS/TD/DO	18	R51	0 R
A - CTS/TD/DO	19	R50	0 R
A - TXSTMS/CS	21	X	X
A - GPIOL0	22	X	X
A - GPIOL1	23	X	X
A - GPIOL2	24	X	X
A - GPIOL3	25	X	X
B - TXD/TK/SK	26	R48	0 R
B - RXD/TD/DO	27	R43	0 R
B - RTS/TD/DO	28	X	X
B - CTS/TD/DO	29	X	X
B - TXSTMS/CS	30	X	X
B - GPIOL0	31	R41	0 R
B - GPIOL1	32	R40	0 R
B - GPIOL2	33	R39	0 R
B - GPIOL3	34	R38	0 R
C - TXD	38	R32	0 R
C - RXD	39	R31	0 R
C - RTS	40	R30	0 R
C - CTS	41	R29	0 R
CDBUS4	43	X	X
CDBUS5	44	X	X
CDBUS6	45	X	X
CDBUS7	46	X	X
D - TXD	48	R38	0 R
D - RXD	52	R45	0 R
D - RTS	53	X	X
D - CTS	54	X	X
DDBUS4	57	X	X
DDBUS5	58	X	X
DDBUS6	59	X	X
DDBUS7	59	X	X

FT4232H

		Texas Instruments 66/3 Bagmane Tech Park CV Raman Nagar Bangalore		
		Title CC31XX EMUBOOST		
Approval	TI	Date	Date	
Designer	TI	Date	Size A2	Project No <Project Number>
Drawn By	TI	Date	Scale	Rev 3.0-A
Layout	TI	Date	Date	Wednesday, April 16, 2014
			Sheet 1 of 3	

